International IOR Rectifier

SAFEIR Series 20ETS12PbF

INPUT RECTIFIER DIODE Lead-Free ("PbF" suffix)

 $V_F < 1V @ 10A$ $I_{FSM} = 300A$ V_{RRM} = 1200V

Description/ Features

The 20ETS12PbF rectifier SAFEIR series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150°C junction temperature.

Typical applications are in input rectification and these products are designed to be used with International Rectifier Switches and Output Rectifiers which are available in identical package outlines.

Output Current in Typical Applications

Applications	Single-phase Bridge	Three-phase Bridge	Units
Capacitive input filter TA = 55°C, TJ = 125°C, common heatsink of 1°C/W	16.3	21	А

Major Ratings and Characteristics

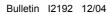
Characteristics	Values	Units
I _{F(AV)} Sinusoidal waveform	20	А
V _{RRM}	1200	V
I _{FSM}	300	А
V _F @10 A, T _J = 25°C	1.0	V
T _J	-40 to 150	°C

Package Outline



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20ETS12PbF SAFEIR Series



International

TOR Rectifier

Voltage Ratings

Part Number	V _{RRM} , maximum peak reverse voltage V	V _{RSM} , maximum non repetitive peak reverse voltage	I _{RRM} 150°C mA
20ETS12PbF	1200	1300	1

Absolute Maximum Ratings

	Parameters	20ETS	Units	Conditions
I _{F(AV)}	Max. Average Forward Current	20	Α	0 T _C = 105° C, 180° conduction half sine wave
I _{FSM}	Max. Peak One Cycle Non-Repetitive	250		10ms Sine pulse, rated V _{RRM} applied
	Surge Current	300	Α	10ms Sine pulse, no voltage reapplied
I ² t	Max. I ² t for fusing	316	A ² s	10ms Sine pulse, rated V _{RRM} applied
		442	7. 3	10ms Sine pulse, no voltage reapplied
I ² √t	Max. I ² √t for fusing	4420	A ² √s	t = 0.1 to 10ms, no voltage reapplied

Electrical Specifications

	Parameters	20ETS	Units	Conditions
V _{FM}	Max. Forward Voltage Drop	1.1	V	@ 20A, T _J = 25°C
r _t	Forward slope resistance	10.4	mΩ	T,= 150°C
V _{F(TO}	Threshold voltage	0.85	V	1, 130 0
I _{RM}	Max. Reverse Leakage Current	0.1	mA	T _J = 25 °C V = rated V
		1.0] '''`	$T_J = 150 ^{\circ}\text{C}$ $V_R = \text{rated } V_{RRM}$

Thermal-Mechanical Specifications

	Parameters		20ETS	Units	Conditions
T _J	Max. Junction Temperature	Range	-40 to 150	°C	
T _{stg}	Max. Storage Temperature	Range	-40 to 150	°C	
R _{thJC}	Max. Thermal Resistance Ju to Case	unction	1.3	°C/W	DC operation
R _{thJA}	Max. Thermal Resistance Ju to Ambient	unction	62	°C/W	(*) For D ² Pak version
R _{thCS}	Typ. Thermal Resistance Cato Heatsink	ase	0.5	°C/W	Mounting surface, smooth and greased
wt	Approximate Weight		2 (0.07)	g(oz.)	
Т	Mounting Torque	Min.	6 (5)	Kg-cm	
		Max.	12 (10)	(lbf-in)	
	Case Style		TO-220AC		
	Device Marking		10ETS	12	

 $^{^*}$ When mounted on 1" square (650mm²) PCB of FR-4 or G-10 material 4 oz (140µm) copper 40°C/W For recommended footprint and soldering techniques refer to application note #AN-994

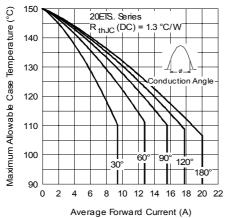


Fig. 1 - Current Rating Characteristics

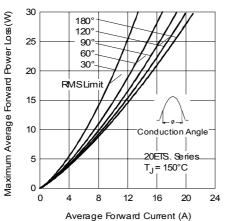


Fig. 3 - Forward Power Loss Characteristics

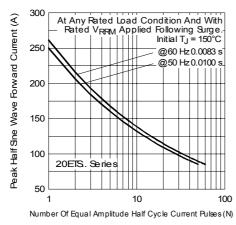


Fig. 5 - Maximum Non-Repetitive Surge Current

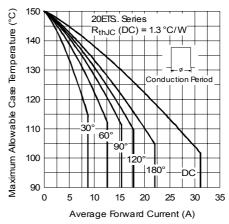


Fig. 2 - Current Rating Characteristics

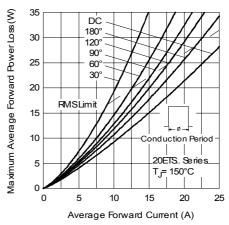


Fig. 4 - Forward Power Loss Characteristics

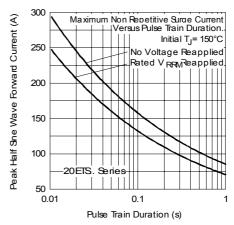


Fig. 6 - Maximum Non-Repetitive Surge Current

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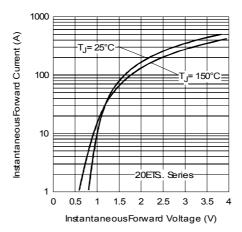


Fig. 7 - Forward Voltage Drop Characteristics

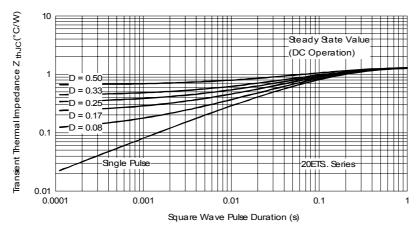
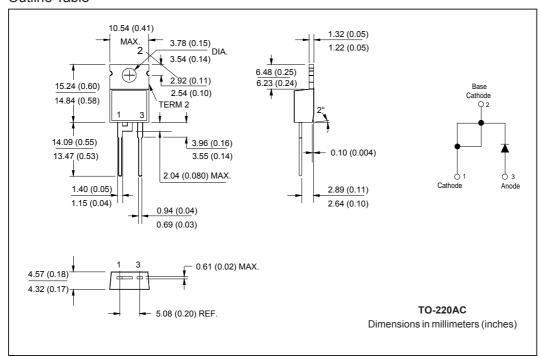
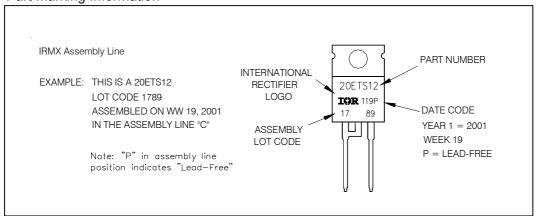


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

Outline Table

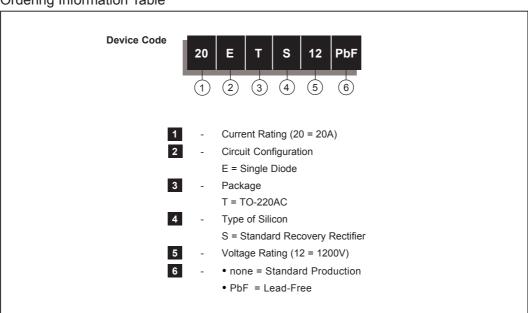


Part Marking Information



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Ordering Information Table



Data and specifications subject to change without notice. This product has been designed and qualified for Industrial Level and Lead-Free.

Qualification Standards can be found on IR's Web site.





Vishay

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