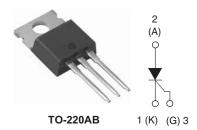


Vishay High Power Products

Phase Control SCR, 25 A



PRODUCT SUMMARY				
V _T at 16 A < 1.25 V				
I _{TSM}	300 A			
V _{RRM}	800/1200 V			

DESCRIPTION/FEATURES

Pk Available

The 25TTS.. High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °

RoHS*

used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter T _A = 55 °C, T _J = 125 °C, common heatsink of 1 °C/W	18	22	А		

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	16	Α		
I _{RMS}		25	A		
V_{RRM}/V_{DRM}		800/1200	V		
I _{TSM}		300	Α		
V _T	16 A, T _J = 25 °C	1.25	V		
dV/dt		500	V/µs		
dI/dt		150	A/µs		
T _J		- 40 to 125	°C		

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
25TTS08PbF	800	800	10			
25TTS12PbF	1200	1200	10			

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COMPITIONS		VALUES		
PARAMETER	SYMBOL TEST CONDITIONS		NUTTIONS	TYP.	MAX.	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 93 °C, 180° conduc	tion half sine wave	wave 16		
Maximum RMS on-state current	I _{RMS}			2	5	Α
Maximum peak, one-cycle,	I	10 ms sine pulse, rated	V _{RRM} applied	30	00	_ ^
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no vol	tage reapplied	3	50	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated	V _{RRM} applied	450		- A ² s
Maximum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied		630		A-S
Maximum $I^2\sqrt{t}$ for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied		63	00	A²√s
Maximum on-state voltage drop	V_{TM}	16 A, T _J = 25 °C		1.	25	V
On-state slope resistance	r _t	T. = 105 °C		12	2.0	mΩ
Threshold voltage	$V_{T(TO)}$	- T _J = 125 °C		1	.0	V
Maximum rayarea and direct lookage current	1 /1	T _J = 25 °C	V - Potod V /V	0.5		
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	$V_R = Rated V_{RRM}/V_{DRM}$		RRM/ V DRM	0	mA
Holding current	l _Η	Anode supply = 6 V, resistive load, initial I _T = 1 A		-	100	IIIA
Maximum latching current	IL	Anode supply = 6 V, resistive load		20	00	
Maximum rate of rise of off-state voltage	dV/dt			50	00	V/µs
Maximum rate of rise of turned-on current	dl/dt			150		A/µs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P_{GM}		8.0	۱۸/	
Maximum average gate power	P _{G(AV)}		2.0	W	
Maximum peak positive gate current	+ I _{GM}		1.5	Α	
Maximum peak negative gate voltage	- V _{GM}		10	V	
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	60	mA	
		Anode supply = 6 V, resistive load, T _J = 25 °C	45		
		Anode supply = 6 V, resistive load, T _J = 125 °C	20		
		Anode supply = 6 V, resistive load, T _J = - 10 °C	2.5		
Maximum required DC gate voltage to trigger	V_{GT}	Anode supply = 6 V, resistive load, T _J = 25 °C	2.0	V	
		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0		
Maximum DC gate voltage not to trigger	V_{GD}	T _J = 125 °C, V _{DRM} = Rated value	0.25		
Maximum DC gate current not to trigger	I _{GD}		2.0	mA	

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9	
Typical reverse recovery time	t _{rr}	T - 105 °C	4	μs
Typical turn-off time	t _q	T _J = 125 °C	110	

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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.1	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque	minimum		6 (5)	kgf · cm	
Mounting torque —	maximum			12 (10)	(lbf \cdot in)
Marking device			Case style TO-220AB 25TTS12		ΓS12

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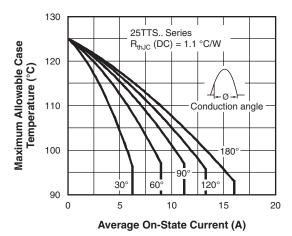
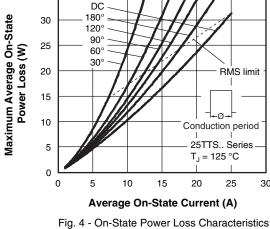


Fig. 1 - Current Rating Characteristics



35

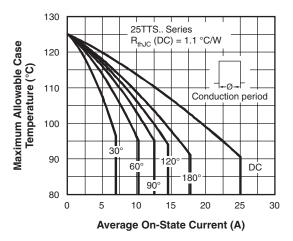


Fig. 2 - Current Rating Characteristics

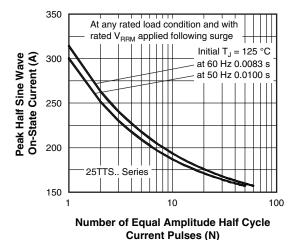


Fig. 5 - Maximum Non-Repetitive Surge Current

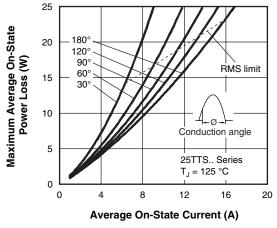


Fig. 3 - On-State Power Loss Characteristics

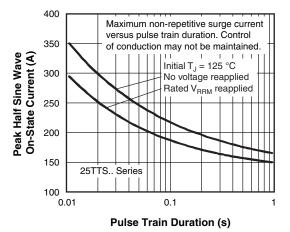


Fig. 6 - Maximum Non-Repetitive Surge Current



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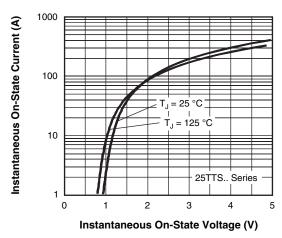


Fig. 7 - On-State Voltage Drop Characteristics

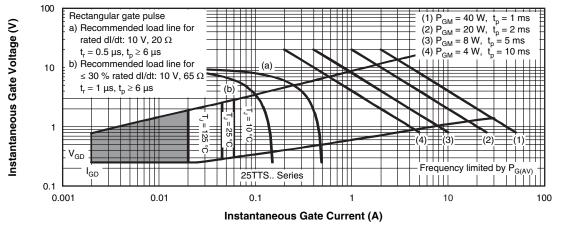


Fig. 8 - Gate Characteristics

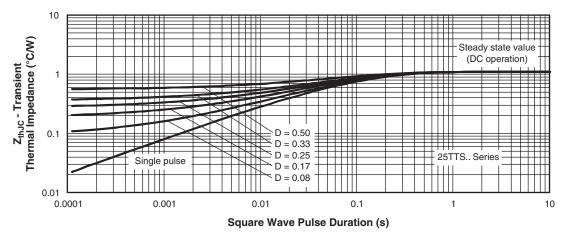
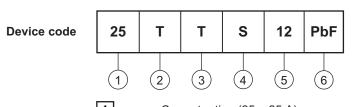


Fig. 9 - Thermal Impedance Z_{thJC} Characteristics

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ORDERING INFORMATION TABLE



Current rating (25 = 25 A)
Circuit configuration:

T = Single thyristor

Package:T = TO-220AB

4 - Type of silicon:

S = Standard recovery rectifier

Voltage rating

08 = 800 V

12 = 1200 V

6 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			



Vishay

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