

DIM1600FSS12-A000

Single Switch IGBT Module

Replaces DS5541-4 July 2014 (LN31768)

FEATURES

- 10µs Short Circuit Withstand
- Non Punch Through Silicon
- Isolated Cu Base with Al₂O₃ Substrates
- Lead Free construction

APPLICATIONS

- High Power Inverters
- Motor Controllers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1600FSS12-A000 is a single switch 1200V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM1600FSS12-A000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		1200V
V _{CE(sat)}	* (typ)	2.2V
l _c ` ´	(max)	1600A
I _{C(PK)}	(max)	3200A

^{*} Measured at the power busbars, not the auxiliary terminals

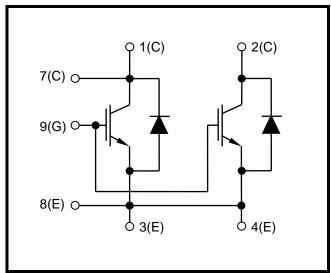


Fig. 1 Circuit configuration



Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	1200	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 85°C	1600	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 110°C	3200	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	13890	W
l ² t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	400	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$R_{\text{th(j-c)}}$	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	9	°C/kW
R _{th(j-c)}	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	20	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T_{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			2	mA
I _{CES}		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			50	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			8	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 80$ mA, $V_{GE} = V_{CE}$	4.5	5.5	6.5	V
V	Collector-emitter	V _{GE} = 15V, I _C = 1600A		2.2	2.8	V
V _{CE(sat)}	saturation voltage	V _{GE} = 15V, I _C = 1600A, T _j = 125°C		2.6	3.3	V
I _F	Diode forward current	DC			1600	Α
I _{FM}	Diode maximum forward current	t _p = 1ms			3200	Α
.,,	Diode forward voltage	I _F = 1600A		2.1	2.4	V
V_{F}		I _F = 1600A, T _j = 125°C		2.1	2.4	V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		180		nF
Qg	Gate charge	±15V		18		μC
C _{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$				nF
L _M	Module inductance			15		nΗ
R _{INT}	Internal transistor resistance			140		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125^{\circ}C, V_{CC} = 900V$ $t_{p} \le 10\mu s, V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*}x dI/dt$ IEC 60747-9		9000		А

Note:

L is the circuit inductance + L_{M}



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time			1250		ns
t _f	Fall time	$I_{C} = 1600A$ $V_{GF} = \pm 15V$		180		ns
E _{OFF}	Turn-off energy loss	$V_{GE} = £13V$ $V_{CE} = 600V$		300		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 1.5\Omega$ $R_{G(OFF)} = 1.5\Omega$ $L_S \sim 100 \text{nH}$		250		ns
t _r	Rise time			200		ns
E _{ON}	Turn-on energy loss			80		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 1600A V _{CE} = 600V		150		μC
I _{rr}	Diode reverse recovery current			750		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 8200A/\mu s$		90		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 1600A		1500		ns
t _f	Fall time			200		ns
E _{OFF}	Turn-off energy loss	$V_{GE} = \pm 15V$ $V_{CE} = 600V$		350		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 1.5\Omega$ $R_{G(OFF)} = 1.5\Omega$ $L_S \sim 100 \text{nH}$		350		ns
t _r	Rise time			220		ns
E _{ON}	Turn-on energy loss			150		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 1600A$ $V_{CE} = 600V$ $dI_F/dt = 7500A/\mu s$		350		μC
I _{rr}	Diode reverse recovery current			900		Α
E _{rec}	Diode reverse recovery energy			160		mJ



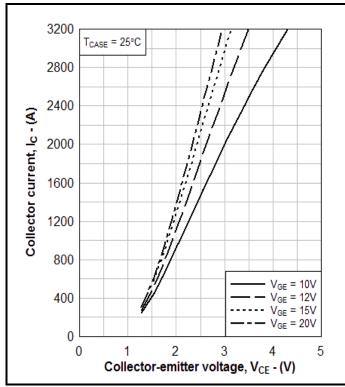


Fig. 3 Typical output characteristics

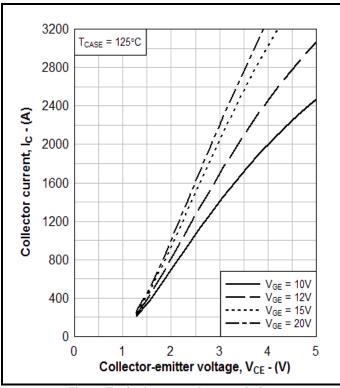


Fig. 4 Typical output characteristics

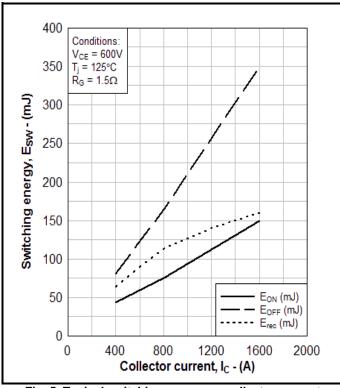


Fig. 5 Typical switching energy vs collector current

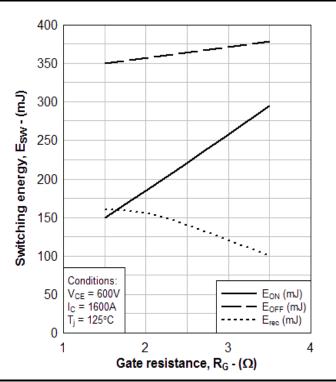


Fig. 6 Typical switching energy vs gate resistance



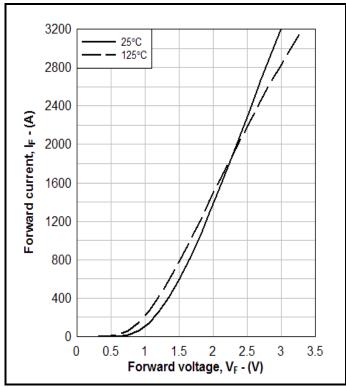


Fig. 7 Diode typical forward characteristics

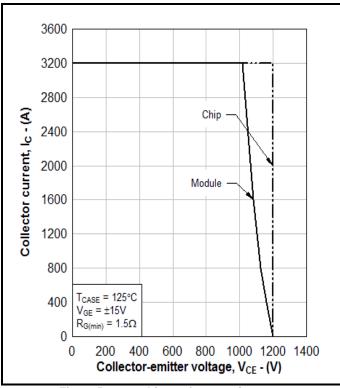


Fig. 8 Reverse bias safe operating area

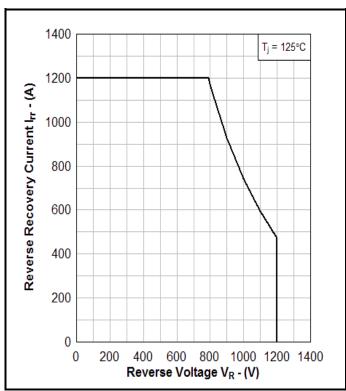


Fig. 9 Diode reverse bias safe operating area

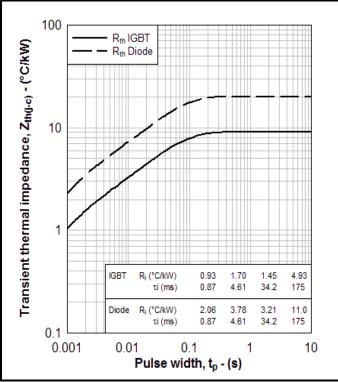


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

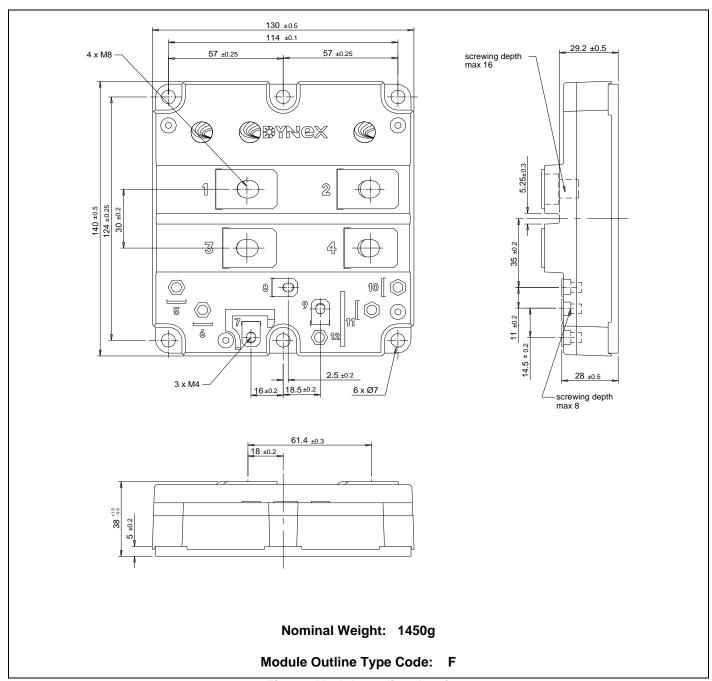


Fig. 11 Module outline drawing



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