

S1109SXU50

Flat Base Rectifier Diode

DS6223-1 February 2018 (LN35118)

FEATURES

- Low losses for high efficiency
- · Hermetically sealed for long operational life
- Easily mounted down with 4 M8 bolts on 46mm centres
- Available with flying lead, full and half bar connections on request
- Available anode to base and cathode to base
- Selections available for parallel operation

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
S1109SXU50 to S1109SXU30	5000 to 3000	$V_{RSM} = V_{RRM} + 100V$

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

\$1109\$XU50 for a 5000V anode to base device **\$1109\$XD50** for a 5000V cathode to base device

KEY PARAMETERS

V_{RRM}	5000V
I _{F(AV)}	470A
I _{FSM}	9200A

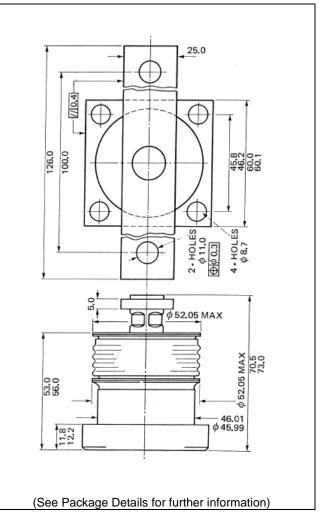


Fig. 1 Package outline



CURRENT RATINGS

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Single Sid	Single Side Cooled (Anode side)					
I _{F(AV)}	Mean forward current	Half wave resistive load	470	А		
I _{F(RMS)}	RMS value	-	736	А		
I _F	Continuous (direct) on-state current	-	580	А		

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 150$ °C	9.2	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	0.441	MA ² s

THERMAL AND MECHANICAL RATINGS

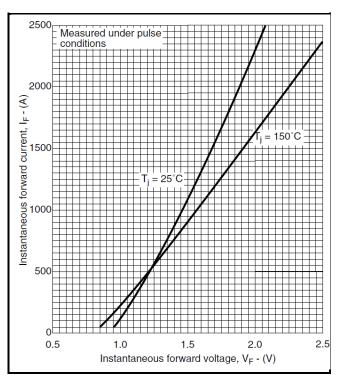
Symbol	Parameter	Test Conditions	Min.	Max.	Units
R _{th(j-c)}	R _{th(j-c)} Thermal resistance – junction to heatsink	dc	-	0.065	°C/W
		Half wave		0.065	°C/W
		3 phase		0.078	°C/W
T_{vj}	Virtual junction temperature	On-state (conducting)	ı	150	°C
		Reverse (blocking)	-	150	°C
T _{stg}	Storage temperature range		-55	165	°C
Torque	Clamping torque		0	22	Nm



CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V_{FM}	Forward voltage	At 1800A peak, T _{case} = 150°C	-	2.1	V
I _{RM}	Peak reverse current	At V _{DRM} , T _{case} = 150°C	-	50	mA
Q _S	Total stored charge	I _F = 1000A, dI _{RR} /dt =3A/µs	-	2800	μC
I _{rr}	Peak reverse recovery current	$T_{case} = 150$ °C, $V_R = 100$ V	-	80	Α
V _{TO}	Threshold voltage	At T _{vj} = 150°C	-	0.88	V
r _T	Slope resistance	At T _{vj} = 150 °C	-	0.687	mΩ

CURVES



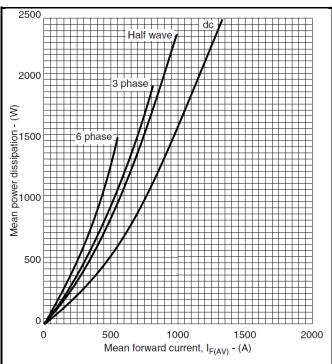
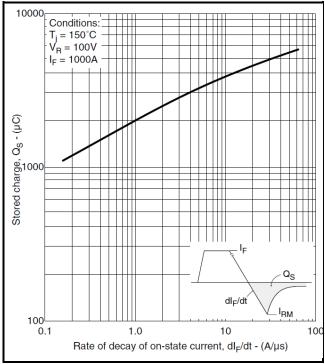


Fig.2 Maximum & minimum on-state characteristics

Fig.3 Dissipation curves





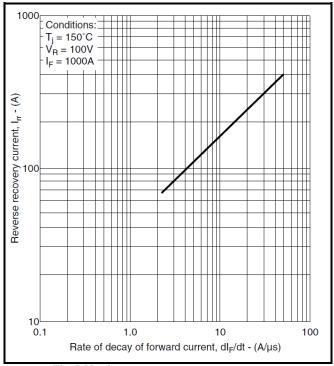
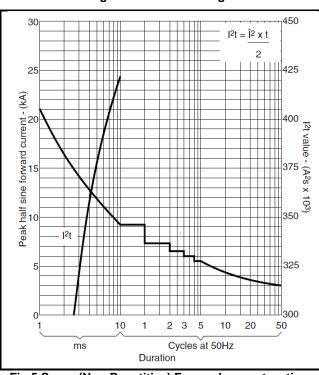


Fig.4 Total stored charge

Fig.5 Maximum reverse recovery current





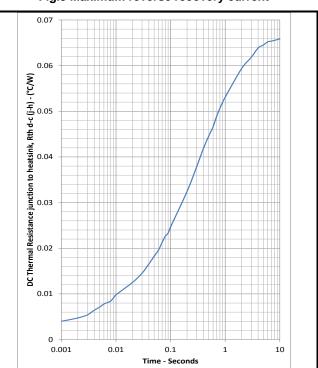
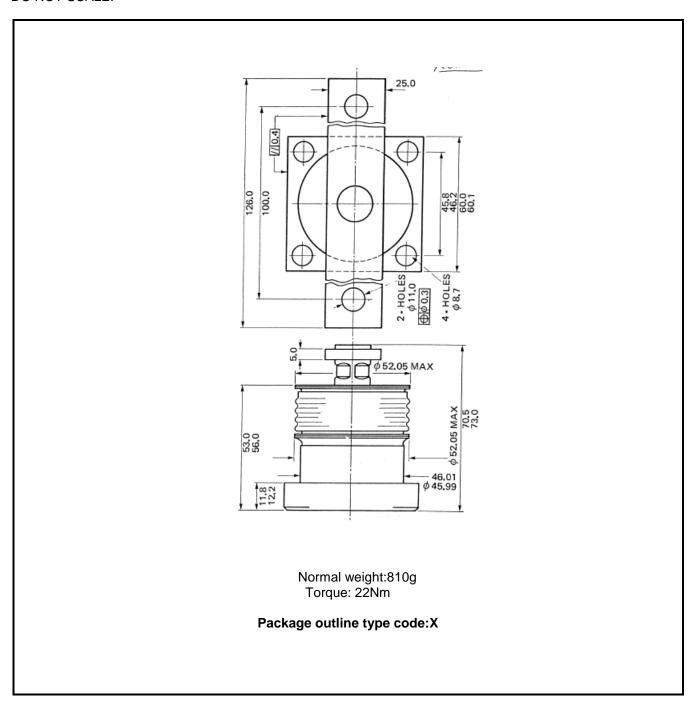


Fig.7 Maximum (limit) transient thermal impedancejunction to heatsink



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.

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No actual design work on the product has been started. **Preliminary Information:**The product design is complete and final charact

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