

SK80MB120CR03TE1



SEMITOP®E1

Half-Bridge (Full SiC)

Engineering Sample SK80MB120CR03TE1

Target Data

Features*

- Optimized design for superior thermal performance
- Extremely low inductance design
- Press-Fit contact technology
- 1200V Planar Gen3 SiC MOS
- Simple to drive with +15V gate voltage
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

- Switched Mode Power Supplies
- Energy Storage Systems
- Electric Vehicle charging
- UPS
- Solar
- Motor Drives

Remarks

- Recommended $T_{j,op} = -40 \dots +150 \text{ °C}$
- Recommended turn-off / turn-on gate voltage $V_{GS} = -4 \dots 0 / +15 \text{ V}$

Footnotes

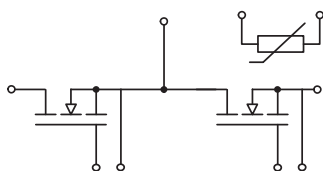
¹⁾ SEMIKRON Exclusive High Performance Thermal Paste (HPTP), available as pre-applied

Absolute Maximum Ratings

Symbol	Conditions	Values	Unit
MOSFET 1			
V_{DSS}		1200	V
I_D	$T_j = 175 \text{ °C}$	$T_s = 25 \text{ °C}$ $T_s = 70 \text{ °C}$	A
I_{DM}	Pulse width t_p limited by T_{jmax}	240	A
$I_{DM,replicative}$		120	A
V_{GS}	Max. transient gate - source voltage	-8 ... 19	V
T_j		-55 ... 175	°C
Integrated body diode			
I_{FM}	Pulse width t_p limited by T_{jmax}	240	A
$I_{FM,replicative}$		120	A

Absolute Maximum Ratings

Symbol	Conditions	Values	Unit
Module			
$I_{t(RMS)}$	$\Delta T_{terminal}$ at PCB joint = 30 K, per pin	30	A
T_{stg}		-40 ... 125	°C
V_{isol}	AC, sinusoidal, $t = 1 \text{ min}$	2500	V



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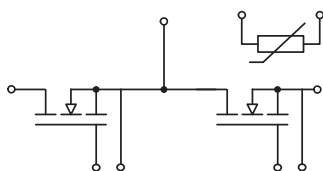
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Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
MOSFET 1					
$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = 0.1 \text{ mA}, T_j = 25 \text{ }^{\circ}\text{C}$	1200			V
$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 23 \text{ mA}, T_j = 25 \text{ }^{\circ}\text{C}$	1.8	2.5	3.6	V
I_{DSS}	$V_{GS} = 0 \text{ V}, V_{DS} = 1200 \text{ V}, T_j = 25 \text{ }^{\circ}\text{C}$			1	mA
I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 15 \text{ V}, T_j = 25 \text{ }^{\circ}\text{C}$			200	nA
$R_{DS(on)}$	$V_{GS} = 15 \text{ V}$				
	$I_D = 83 \text{ A}$				
	$T_j = 25 \text{ }^{\circ}\text{C}$		16	22	mΩ
	chipllevel		25		mΩ
C_{iss}	$V_{GS} = 0 \text{ V}, V_{DS} = 1000 \text{ V}, f = 0.1 \text{ MHz}$		6800		pF
C_{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 1000 \text{ V}, f = 0.1 \text{ MHz}$		260		pF
C_{rss}	$V_{GS} = 0 \text{ V}, V_{DS} = 1000 \text{ V}, f = 0.1 \text{ MHz}$		20		pF
R_{Gint}	$T_j = 25 \text{ }^{\circ}\text{C}$		5.9		Ω
Q_G	$V_{DD} = 800 \text{ V}, V_{GS} = -4 \text{ V} \dots 15 \text{ V}, I_D = 83 \text{ A}$		236		nC
$t_{d(on)}$	$V_{DD} = 600 \text{ V}$		22		ns
$t_{d(off)}$	$V_{GS} = 15/-4 \text{ V}$		80		ns
t_r	$I_D = 80 \text{ A}$		10		ns
t_f	$R_{G on/off} = 0 \text{ }^{\circ}\Omega$		11		ns
E_{on}	$di/dt_{off} = 12 \text{ kA}/\mu\text{s}$		1.14		mJ
E_{off}	$di/dt_{on} = 10 \text{ kA}/\mu\text{s}$		0.72		mJ
$R_{th(j-s)}$	$per \text{ MOSFET}, \lambda_{paste} = 2.5 \text{ W}/(\text{mK})$ ¹⁾		0.51		K/W
Integrated body diode					
$V_F = V_{SD}$	$-I_D = 41 \text{ A}$		4.6		V
	$V_{GS} = -4 \text{ V}$				
	chipllevel		4.3		V
$V_{F0} = V_{SD0}$			3.8		V
	chipllevel		3.6		V
$r_F = r_{SD}$			19		mΩ
	chipllevel		17		mΩ
t_{rr}	$V_{DD} = 600 \text{ V}$		32		ns
Q_{rr}	$-I_D = 80 \text{ A}$		1.6		μC
I_{rr}	$V_{GS} = -4 \text{ V}$		100		A
E_{rr}	$R_{Gon} = 0 \text{ }^{\circ}\Omega$		0.36		mJ
	$di/dt_{off} = 10 \text{ kA}/\mu\text{s}$				

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Module					
L_{CE}			9		nH
M_s	to heatsink	1.6		2.3	Nm
w	weight		25		g

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Temperature Sensor					
R_{100}	$T_r = 100 \text{ }^{\circ}\text{C}$		$493 \pm 5\%$		Ω
$B_{100/125}$	$R(T) = R_{100} \exp[B_{100/125}(1/T - 1/T_{100})]; T[K];$		$3550 \pm 2\%$		K



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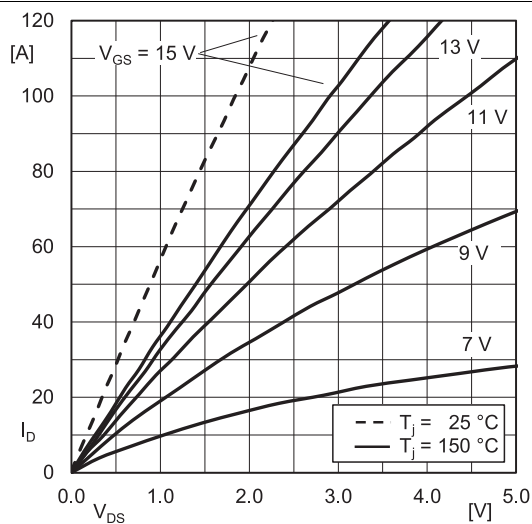


Fig. 1: Typ. MOSFET forward output characteristic, incl. $R_{DS(on)} + SS'$

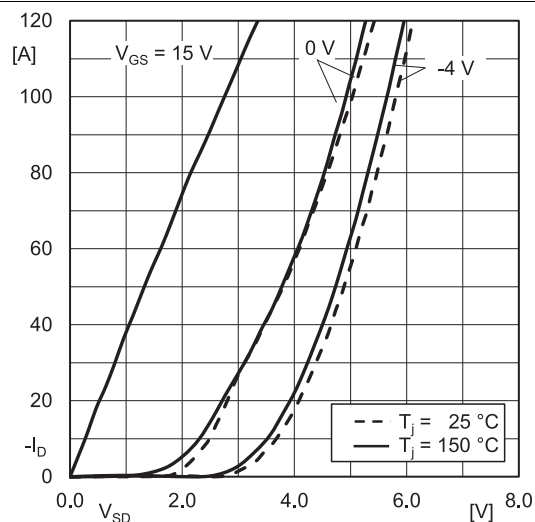


Fig. 2: Typ. MOSFET reverse output characteristics, incl. $R_{DS(on)} + SS'$

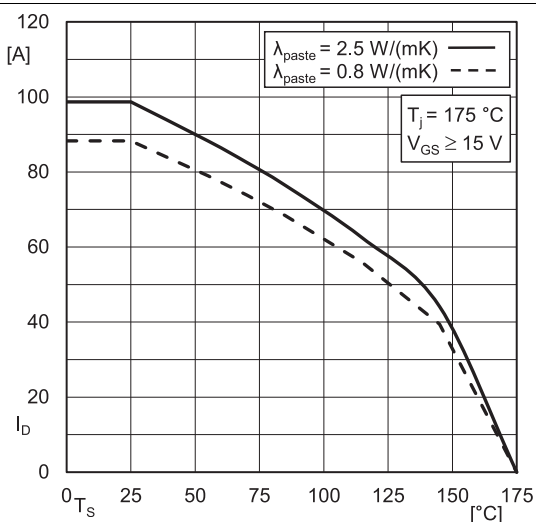


Fig. 3: Rated current vs. temperature $I_D = f(T_S)$

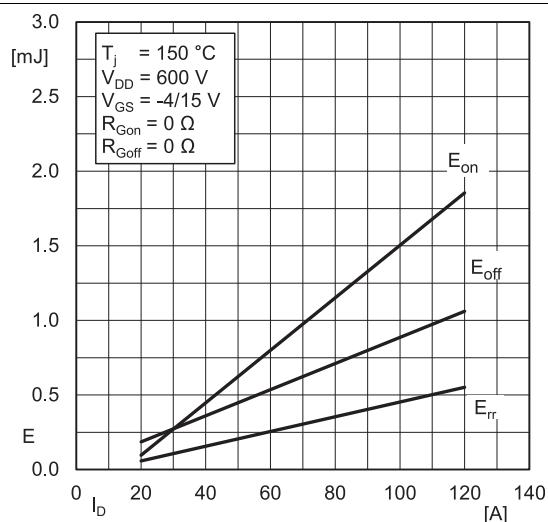


Fig. 4: Typ. turn-on/-off energy $E = f(I_D)$

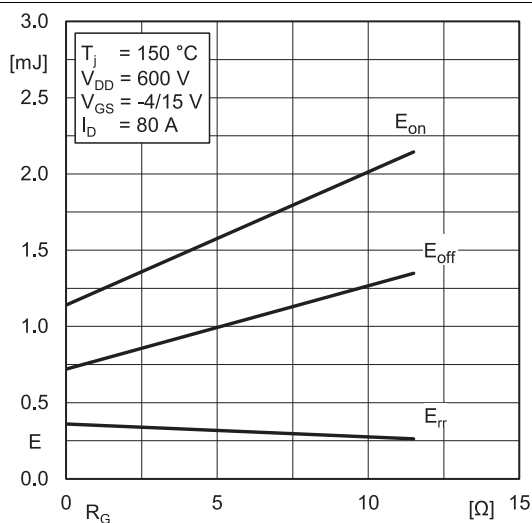


Fig. 5: Typ. turn-on /-off energy $E = f(R_G)$

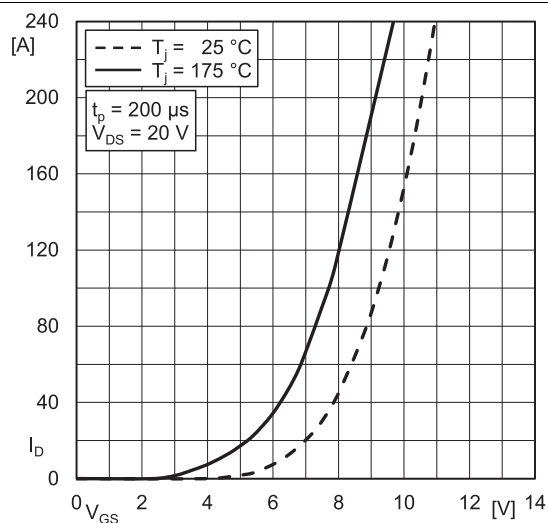


Fig. 6: Typ. MOSFET transfer characteristic

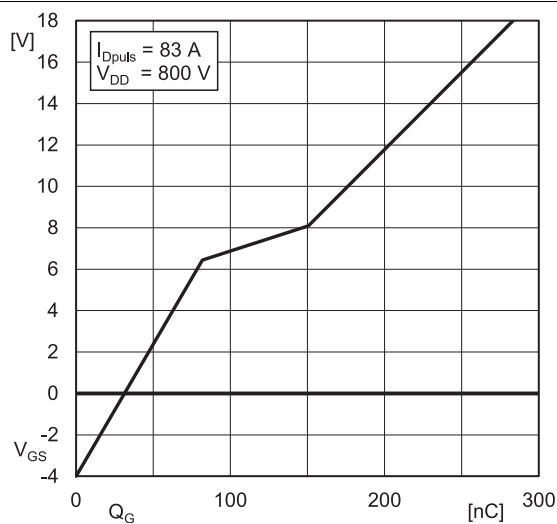


Fig. 7: Typ. MOSFET gate charge characteristic

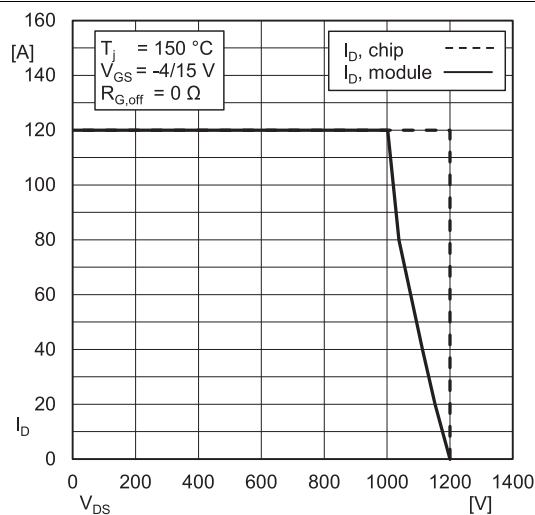


Fig. 12: MOSFET Reverse Bias Safe Operating Area (RBSOA)

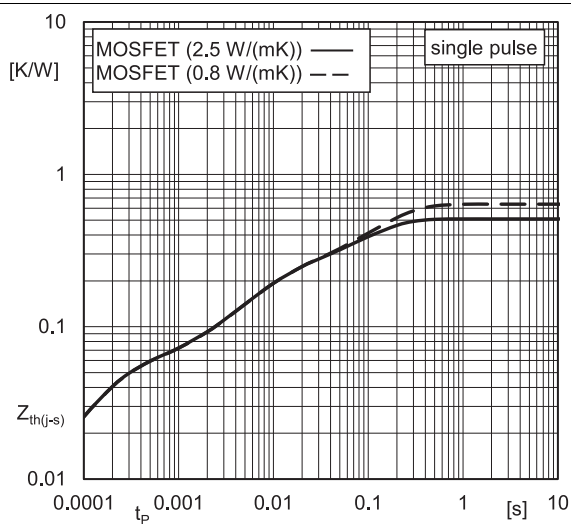
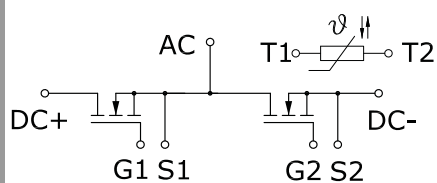
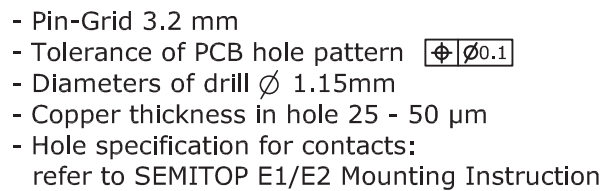


Fig. 13: Typ. transient thermal impedance



This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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