

SEMITRANS® 3

IGBT4 Modules

SKM150GB17E4G

Target Data

Features

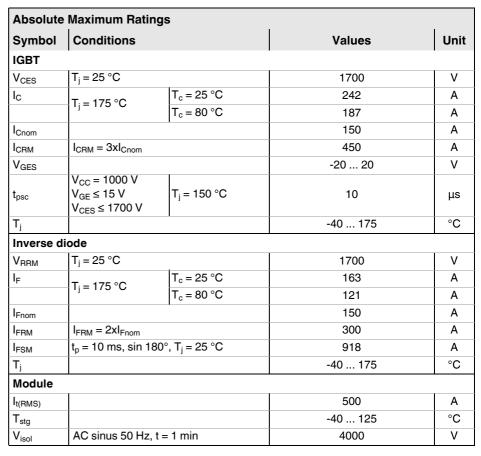
- IGBT4 = 4. generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4. Generation **CAL-Diode**
- Insulated copper baseplate using DBC Technology (Direct Copper Bonding)
- With integrated Gate resistor
- For switching frequenzies up to 8kHz
- UL recognized, file no. E63532

Typical Applications*

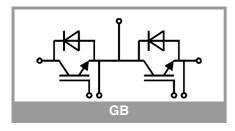
- · AC inverter drives
- UPS
- · Electronic welders
- · Wind power
- · Public transport

Remarks

- · Case temperature limited to $T_c = 125$ °C max.
- Recommended $T_{op} = -40 \dots +150$ °C
- Product reliability results valid for $T_i = 150$ °C



Characte	Characteristics								
Symbol	Conditions	min.	typ.	max.	Unit				
IGBT									
V _{CE(sat)}	I _C = 150 A	T _j = 25 °C		1.90	2.21	V			
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.35	2.60	V			
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V			
		T _j = 150 °C		0.70	0.80	V			
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		7.3	8.7	mΩ			
		T _j = 150 °C		11	12	mΩ			
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_{C}=6$ mA		5.2	5.8	6.4	V			
I _{CES}	V _{GE} = 0 V V _{CE} = 1700 V	T _j = 25 °C			2.0	mA			
		T _j = 150 °C		-		mA			
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		13.6		nF			
C _{oes}		f = 1 MHz		0.53		nF			
C _{res}		f = 1 MHz		0.44		nF			
Q_{G}	V _{GE} = - 8 V+ 15 V			1200		nC			
R _{Gint}	T _j = 25 °C			4.3		Ω			
t _{d(on)}	$V_{CC} = 1200 \text{ V}$ $I_{C} = 150 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1 \Omega$ $R_{G \text{ off}} = 1 \Omega$	T _j = 150 °C		205		ns			
t _r		T _j = 150 °C		23.5		ns			
E _{on}		T _j = 150 °C		39		mJ			
t _{d(off)}		T _j = 150 °C		550		ns			
t _f		T _j = 150 °C		150		ns			
E _{off}		T _j = 150 °C		59		mJ			
R _{th(j-c)}	per IGBT	1			0.161	K/W			
R _{th(c-s)}	per IGBT (λ _{grease} =0		0.035		K/W				





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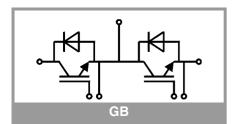
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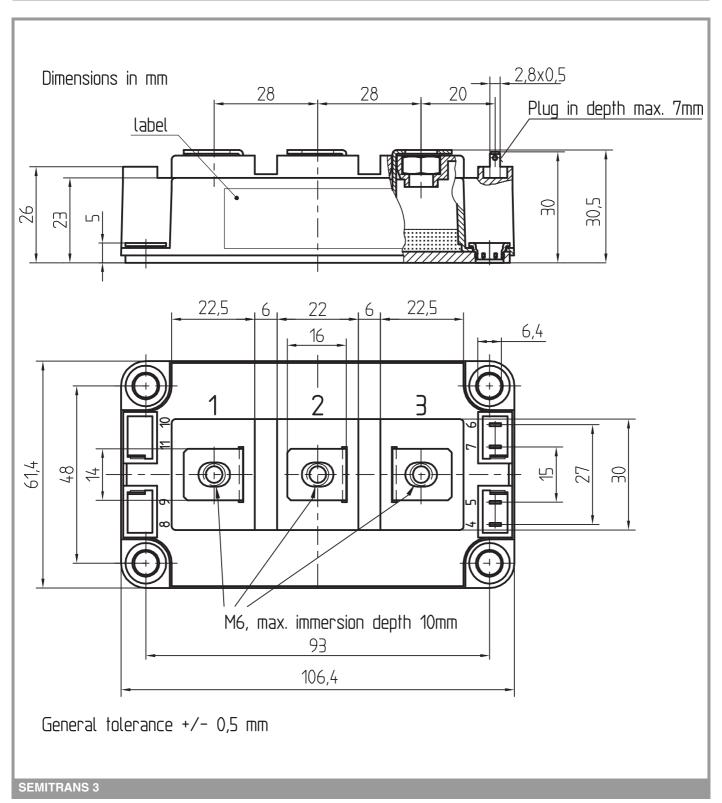
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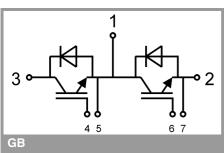
Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for $T_j = 150$ °C

Characteristics										
Symbol	Conditions	min.	typ.	max.	Unit					
Inverse diode										
$V_F = V_{EC}$	$I_F = 150 \text{ A}$ $V_{GE} = 0 \text{ V}$ chiplevel	T _j = 25 °C		2.00	2.40	V				
		T _j = 150 °C		2.14	2.56	V				
V _{F0}	chiplevel	T _j = 25 °C		1.32	1.56	V				
		T _j = 150 °C		1.08	1.22	V				
r _F	chiplevel	T _j = 25 °C		4.5	5.6	mΩ				
		T _j = 150 °C		7.1	9.0	mΩ				
I _{RRM}	I _F = 150 A	T _j = 150 °C		185		Α				
Q _{rr}	V _{GE} = ±15 V V _{CC} = 1200 V	T _j = 150 °C		49		μC				
E _{rr}		T _j = 150 °C		33		mJ				
R _{th(j-c)}	per diode			0.356	K/W					
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.039		K/W				
Module										
L _{CE}				15		nΗ				
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.55		mΩ				
		T _C = 125 °C		0.85		mΩ				
R _{th(c-s)1}	calculated without		0.009		K/W					
R _{th(c-s)2}	including thermal c Ts underneath mod (m*K))		0.015		K/W					
Ms	to heat sink M6		3		5	Nm				
M _t		to terminals M6	2.5		5	Nm				
						Nm				
w		_			325	g				







This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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