SKiM® 93

Trench IGBT Modules

SKiM429GD17E44F

Features

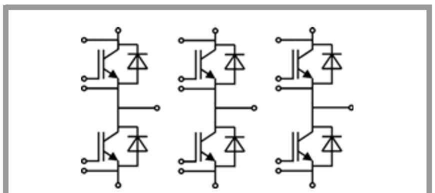
- IGBT 4 Trench Gate Technology
- Solderless sinter technology
- $V_{CE(sat)}$ with positive temperature coefficient
- Low inductance case
- Insulated by Al_2O_3 DBC (Direct Bonded Copper) ceramic substrate
- Pressure contact technology for thermal contacts
- Spring contact system to attach driver PCB to the control terminals
- High short circuit capability, self limiting to 6 x I_C
- Integrated temperature sensor

Typical Applications*

- Automotive inverter
- High reliability AC inverter wind
- High reliability AC inverter drives

Remarks

- Case temperature limited to T_s = 125°C max; $T_c = T_s$ (for baseplateless modules)
- Recommended T_{op} = -40 … +150°C



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Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
Inverter - IGBT				
V_{CES}	T_j = 25 °C		1700	V
I_C	λ_{paste}=0.8 W/(mK) T_j = 175 °C	T_s = 25 °C	608	A
		T_s = 70 °C	489	A
I_C	λ_{paste}=2.5 W/(mK) T_j = 175 °C	T_s = 25 °C	789	A
		T_s = 70 °C	639	A
I_{Cnom}			420	A
I_{CRM}	I_{CRM} = 3 x I_{Cnom}		1260	A
V_{GES}			-20 ... 20	V
t_{psc}	V_{CC} = 1000 V, $V_{GE} \leq$ 15 V, $V_{CES} \leq$ 1700 V	T_j = 150 °C	10	µs
T_j			-40 ... 175	°C
Inverse - Diode				
I_F	λ_{paste}=0.8 W/(mK) T_j = 175 °C	T_s = 25 °C	394	A
		T_s = 70 °C	308	A
I_F	λ_{paste}=2.5 W/(mK) T_j = 175 °C	T_s = 25 °C	482	A
		T_s = 70 °C	379	A
I_{Fnom}			450	A
I_{FRM}	I_{FRM} = 2 x I_{Fnom}		900	A
I_{FSM}	t_p = 10 ms, sin 180°, T_j = 150 °C		2322	A
T_j			-40 ... 175	°C
Module				
$I_{t(RMS)}$	$T_{terminal}$ = 80 °C,		700	A
T_{stg}			-40 ... 125	°C
V_{isol}	AC sinus 50 Hz, t = 1 min		3000	V

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
Inverter - IGBT						
$V_{CE(sat)}$	I_C = 420 A, V_{GE} = 15 V, chiplevel	T_j = 25 °C		1.90	2.25	V
		T_j = 150 °C		2.25	2.45	V
V_{CE0}	chiplevel	T_j = 25 °C		1.10	1.20	V
		T_j = 150 °C		1.00	1.10	V
r_{CE}	V_{GE} = 15 V, chiplevel	T_j = 25 °C		1.90	2.5	mΩ
		T_j = 150 °C		3.0	3.2	mΩ
$V_{GE(th)}$	$V_{GE} = V_{CE}$, I_C = 16.8 mA		5.2	5.8	6.4	V
I_{CES}	V_{GE} = 0 V, V_{CE} = 1700 V, T_j = 25 °C			0.15	0.5	mA
C_{ies}	V_{CE} = 25 V, V_{GE} = 0 V	f = 1 MHz		33		nF
C_{oes}		f = 1 MHz		1.38		nF
C_{res}		f = 1 MHz		1.08		nF
Q_G	V_{GE} = - 8 V...+ 15 V			3360		nC
R_{Gint}	T_j = 25 °C			2.7		Ω
$t_{d(on)}$	V_{CC} = 1200 V, I_C = 420 A, $R_{G\,on}$ = 2.2 Ω, $R_{G\,off}$ = 2.2 Ω, di/dt$_{on}$ = 7450 A/µs, di/dt$_{off}$ = 1920 A/µs	T_j = 150 °C		498		ns
t_r		T_j = 150 °C		62		ns
E_{on}		T_j = 150 °C		178		mJ
$t_{d(off)}$		T_j = 150 °C		922		ns
t_f		T_j = 150 °C		220		ns
E_{off}	V_{GE} = +15/-15 V	T_j = 150 °C		189		mJ
$R_{th(j-s)}$	per IGBT, λ_{paste}=0.8 W/(mK)			0.079		K/W
$R_{th(j-s)}$	per IGBT, λ_{paste}=2.5 W/(mK)			0.051		K/W

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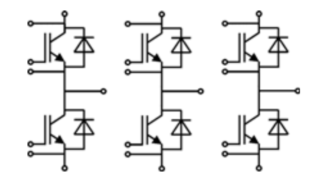

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Characteristics						
Symbol	**Conditions**		**min.**	**typ.**	**max.**	**Unit**
Inverse - Diode						
$V_F = V_{EC}$	I_F = 420 A V_{GE} = 0 V chiplevel	T_j = 25 °C		1.93	2.32	V
		T_j = 150 °C		2.04	2.43	V
V_{F0}	chiplevel	T_j = 25 °C		1.32	1.56	V
		T_j = 150 °C		1.08	1.22	V
r_F	chiplevel	T_j = 25 °C		1.46	1.80	mΩ
		T_j = 150 °C		2.3	2.9	mΩ
I_{RRM}	I_F = 420 A di/dt$_{off}$ = 7630 A/µs V_{GE} = +15/-15 V V_{CC} = 1200 V	T_j = 150 °C		577		A
Q_{rr}		T_j = 150 °C		150		µC
E_{rr}		T_j = 150 °C		119		mJ
$R_{th(j-s)}$	per Diode, λ_{paste}=0.8 W/(mK)			0.169		K/W
$R_{th(j-s)}$	per Diode, λ_{paste}=2.5 W/(mK)			0.125		K/W
Module						
L_{CE}				10	15	nH
$R_{CC'+EE'}$	measured per switch	T_s = 25 °C		0.3		mΩ
		T_s = 125 °C		0.5		mΩ
w				1042		g
Temperature Sensor						
R_{100}	T_r=100°C (R_{25}=1000Ω)			1670 ± 1%		Ω
R(T)	R(T)=1kΩ[1+A(T-25°C)+B(T-25°C)^2], A = 7.64*10^{-3}°C^{-1}, B = 1.73*10^{-5}°C^{-2}					

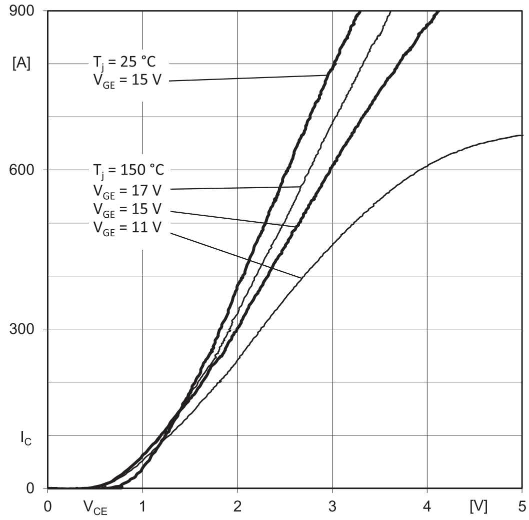


Fig. 1: Typ. output characteristic, inclusive $R_{CC'+ EE'}$

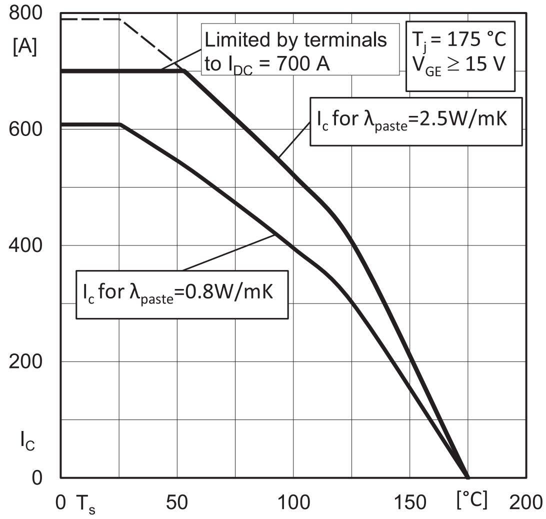


Fig. 2: Typ. rated current vs. temperature $I_C = f(T_S)$

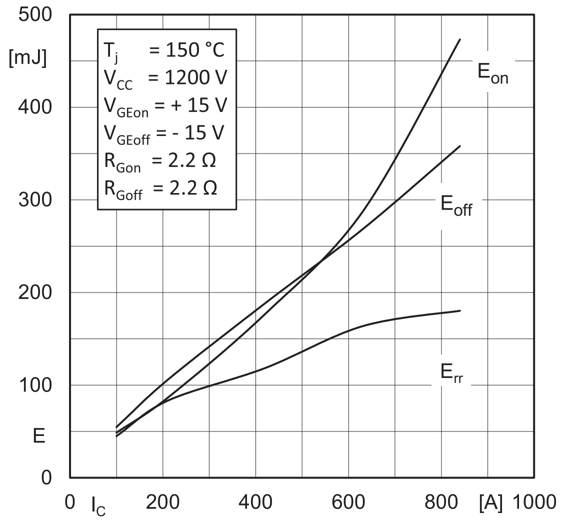


Fig. 3: Typ. turn-on /-off energy = f (I_C)

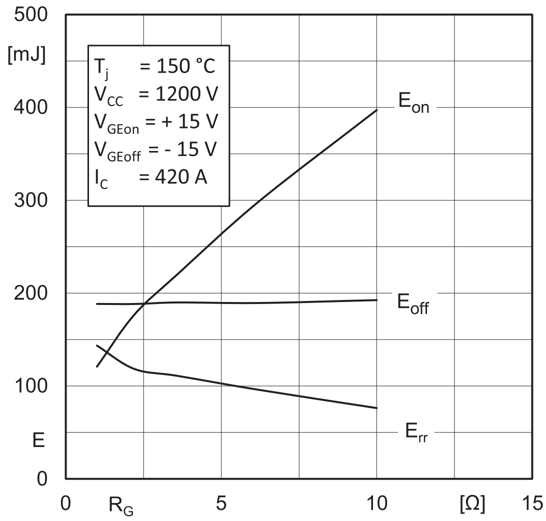


Fig. 4: Typ. turn-on /-off energy = f (R_G)

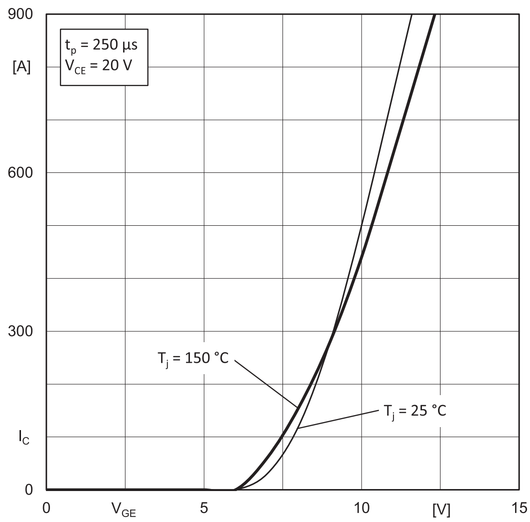


Fig. 5: Typ. transfer characteristic

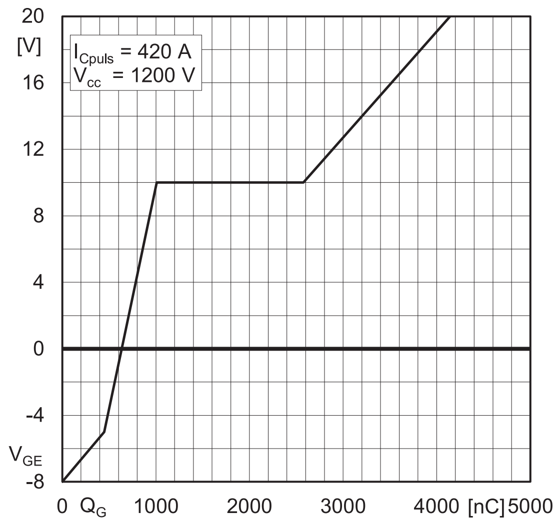


Fig. 6: Typ. gate charge characteristic

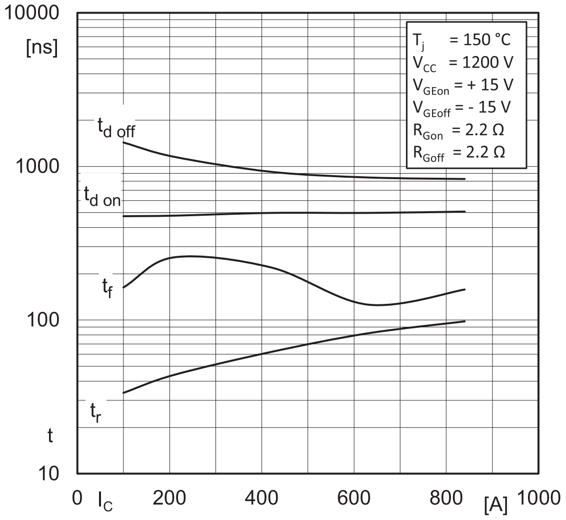


Fig. 7: Typ. switching times vs. I_C

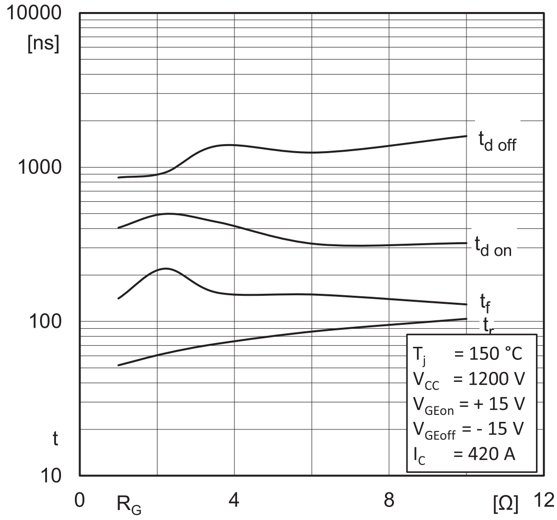


Fig. 8: Typ. switching times vs. gate resistor R_G

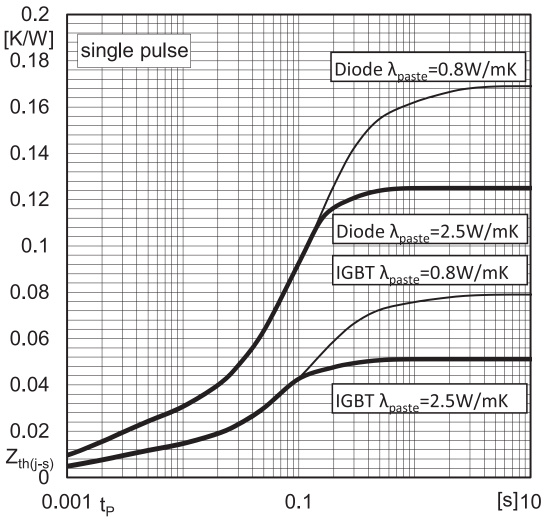


Fig. 9: Typ. transient thermal impedance

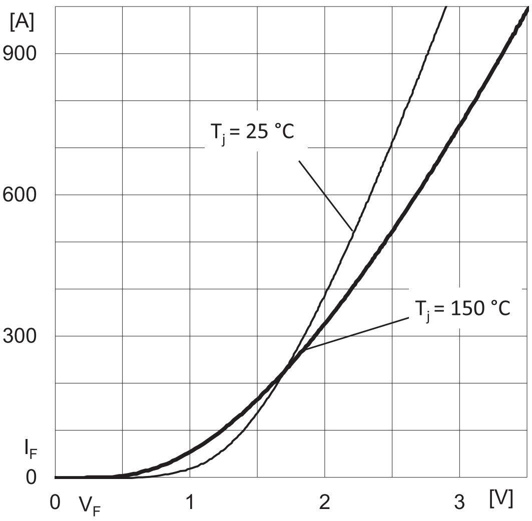


Fig. 10: Typ. CAL diode forward charact., incl. $R_{CC'+EE'}$

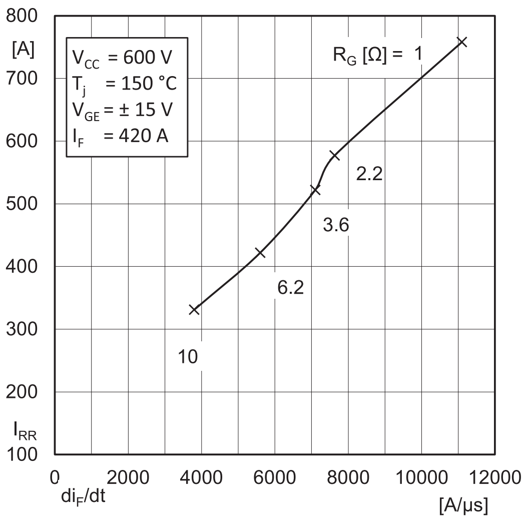


Fig. 11: Typ. CAL diode peak reverse recovery current

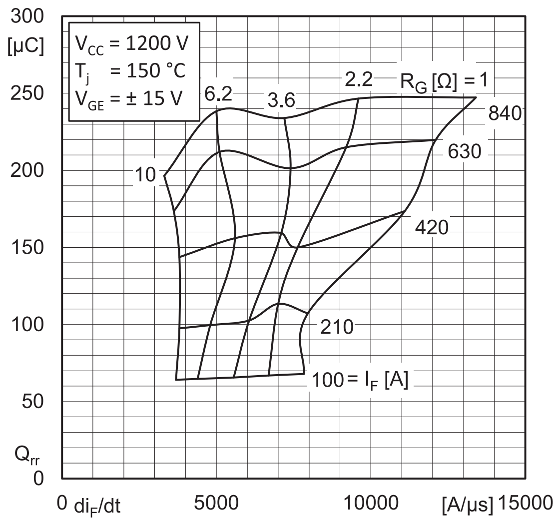
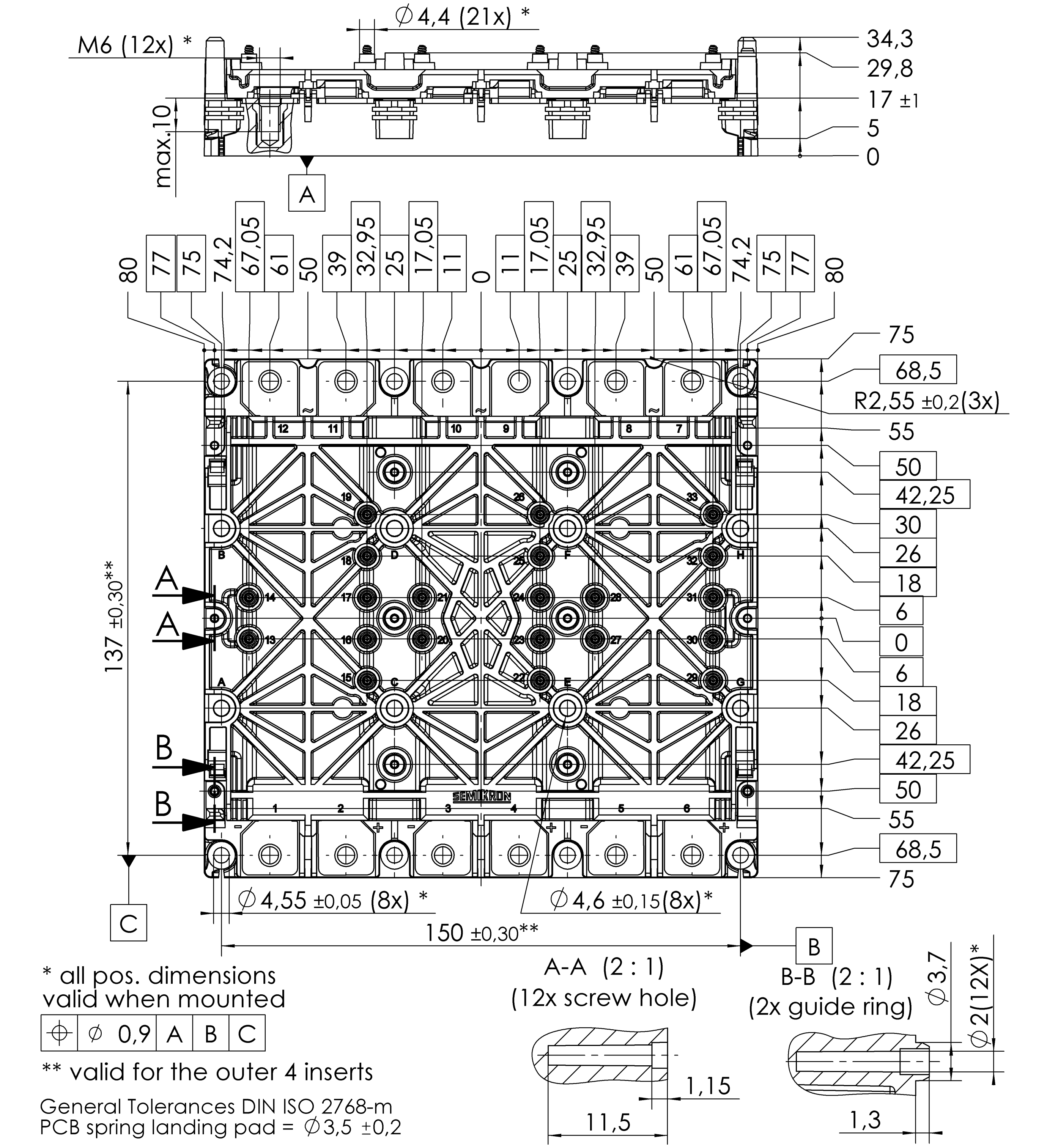
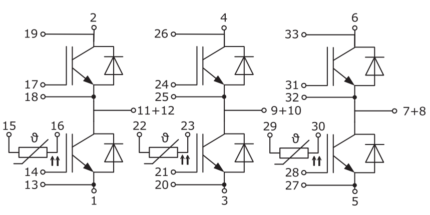


Fig. 12: Typ. CAL diode recovery charge


∅4,4 (21x) *
M6 (12x) *
max.10
34,3
29,8
17 ±1
5
0
A
R2,55 ±0,2(3x)
137 ±0,30**
∅4,55 ±0,05 (8x) *
∅4,6 ±0,15(8x)*
150 ±0,30**
C
B
A-A (2 : 1)
(12x screw hole)
B-B (2 : 1)
(2x guide ring)
∅3,7
∅2(12X)*
1,15
11,5
1,3
* all pos. dimensions
valid when mounted
∅ 0,9 A B C
** valid for the outer 4 inserts
General Tolerances DIN ISO 2768-m
PCB spring landing pad = ∅3,5 ±0,2



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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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