

TG450HF17M1-S300

Half Bridge IGBT Module

DS6253-1 August 2018 (LN36247)

FEATURES

- Cu Base with Enhanced Al₂O₃ Substrates
- 10µs Short Circuit Withstand

APPLICATIONS

- Motor Drives
- · Power Charging Equipment
- Reactive Compensation
- High Reliability Inverters

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The TG450HF17M1-S300 is a half bridge 1700V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus $10\mu s$ short circuit withstand.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

TG450HF17M1-S300

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		1700V
V _{CE(sat)}	* (typ)	1.80V
I _C	(max)	450A
I _{C(RM)}	(max)	900A

^{*} Measured at the auxiliary terminals

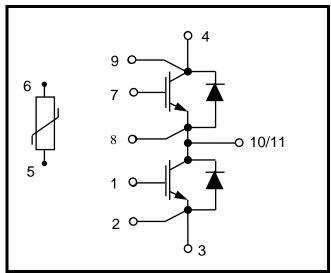


Fig. 1 Circuit configuration



Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V, T_{C} = 25^{\circ}C$	1700	V
V_{GES}	Gate-emitter voltage	T _C = 25°C	±20	V
I _C	Continuous collector current	T _C = 95°C	450	Α
I _{C(PK)}	Peak collector current	$t_P = 1 \text{ms}, T_C = 125^{\circ}\text{C}$	900	Α
P _{max}	Max. transistor power dissipation	$T_C = 25^{\circ}C, T_{vj} = 150^{\circ}C$	2270	W
l ² t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_{vj} = 150$ °C	16.2	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	3400	V

THERMAL AND MECHANICAL RATINGS

Internal insulation material: Al_2O_3 Baseplate material: Cu Creepage distance – Terminal to heatsink: 14.5mm Creepage distance – Terminal to terminal: 13.0mm Clearance – Terminal to heatsink: 12.5mm Clearance – Terminal to terminal: 10mm

CTI (Comparative Tracking Index): >200

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – IGBT	Continuous dissipation -	-	-	55	°C/kW
R _{th(j-c)}	Thermal resistance – diode	junction to case	-	-	95	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (IGBT)	Mounting torque 3Nm	-	-	28	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (Diode)	(with mounting grease 1W/m °C)	-	-	48	°C/kW
_	Junction temperature	IGBT	-40	-	150	°C
T _j		Diode	-40	-	150	°C
T _{stg}	Storage temperature range	-	-40	-	150	°C
	Screw torque	Mounting – M5	3	-	6	Nm
		Electrical connections – M6	3	-	6	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions Min 1		Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			1	mA
I _{CES}		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{C} = 125^{\circ}C$			10	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{C} = 150^{\circ}C$			20	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V$, $V_{CE} = 0V$			0.5	μA
$V_{\text{GE(TH)}}$	Gate threshold voltage	$I_C = 15$ mA, $V_{GE} = V_{CE}$	5.0	6.0	7.0	V
		$V_{GE} = 15V, I_{C} = 450A$		1.8	2.2	V
$V_{\text{CE(sat)}}$	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 450A, T_j = 125^{\circ}C$		2.1	2.5	V
	voltago	$V_{GE} = 15V, I_C = 450A, T_j = 150^{\circ}C$		2.2	2.6	V
I _F	Diode forward current	DC		450		Α
I _{FM}	Diode maximum forward current	$t_p = 1 ms$		900		Α
	Diode forward voltage	I _F = 450A		2.1	2.4	V
V_{F}		I _F = 450A, T _j = 125°C		2.2	2.6	V
		I _F = 450A, T _j = 150°C		2.2	2.6	V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 100kHz$		42		nF
Q_g	Gate charge	±15V		4.4		μC
C _{res}	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		1.2		nF
L _M	Module inductance			20		nΗ
R _{INT}	Internal transistor resistance			0.9		mΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 150^{\circ}\text{C}, V_{CC} = 1000\text{V}$ $t_{p} \le 10\mu\text{s}, V_{GE} \le 15\text{V}$ $V_{CE (max)} = V_{CES} - L^{*} x dl/dt$ IEC 60747-9		2000		А

Note:

L is the circuit inductance $+ L_M$

NTC-Thermistor Data

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
R ₂₅	Rated resistance	$T_{\rm C} = 25^{\circ}{\rm C}$		5		kΩ
Δ <i>R</i> /R	Deviation of R100	$T_{\rm C} = 100^{\circ}{\rm C}, {\rm R}_{100} = 493\Omega$	-5		5	%
P ₂₅	Power dissipation	T _C = 25°C			20	m/W
B _{25/50}		$R_2 = R_{25} exp [B_{25/50}(1/T2 - 1/(298.15K))]$		3375		K
B _{25/80}	B-value	$R_2 = R_{25} exp [B_{25/80}(1/T2 - 1/(298.15K))]$		3411		K
B _{25/100}		$R_2 = R_{25} exp [B_{25/100}(1/T2 - 1/(298.15K))]$		3433		K



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$\begin{array}{c} I_{C} = 450 A \\ V_{CE} = 900 V \\ V_{GE} = \pm 15 V \\ R_{G(OFF)} = 2.7 \Omega \\ R_{G(ON)} = 2.7 \Omega \\ L_{S} \sim 40 nH \end{array}$			890		ns
t _f	Fall time		<i>dv/dt</i> = 4500V/μs		560		ns
E _{OFF}	Turn-off energy loss				155		mJ
t _{d(on)}	Turn-on delay time		di/dt = 6400A/µs		165		ns
t _r	Rise time				65		ns
E _{ON}	Turn-on energy loss				53		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 450A V _{CE} = 900V			85		μC
I _{rr}	Diode reverse recovery current				425		Α
E _{rec}	Diode reverse recovery energy	di/dt = 6	3400A/µs		65		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$I_{C} = 450A$ $V_{CE} = 900V$ $V_{GE} = \pm 15V$ $R_{G(OFF)} = 2.7\Omega$ $R_{G(ON)} = 2.7\Omega$ $L_{S} \sim 40nH$	<i>dv/dt</i> = 4500V/μs		940		ns
t _f	Fall time				730		ns
E _{OFF}	Turn-off energy loss				185		mJ
t _{d(on)}	Turn-on delay time		di/dt = 6400A/µs		155		ns
t _r	Rise time				75		ns
E _{ON}	Turn-on energy loss				80		mJ
Q _{rr}	Diode reverse recovery charge	$I_F = 450A$ $V_{CE} = 900V$			125		μC
I _{rr}	Diode reverse recovery current				436		Α
E _{rec}	Diode reverse recovery energy	di/dt = 6	4400A/µs		94		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$\begin{array}{c} I_{C} = 450A \\ V_{CE} = 900V \\ V_{GE} = \pm 15V \\ R_{G(OFF)} = 2.7\Omega \\ R_{G(ON)} = 2.7\Omega \\ L_{S} \sim 40 nH \end{array}$	<i>dv/dt</i> = 4500V/μs		960		ns
t _f	Fall time				760		ns
E _{OFF}	Turn-off energy loss				190		mJ
t _{d(on)}	Turn-on delay time		<i>di/dt</i> = 6400A/µs		150		ns
t _r	Rise time				75		ns
E _{ON}	Turn-on energy loss				86		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 450A			145		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 900V$		455		Α	
E _{rec}	Diode reverse recovery energy	<i>di/dt</i> = 6	3400A/µs		105		mJ



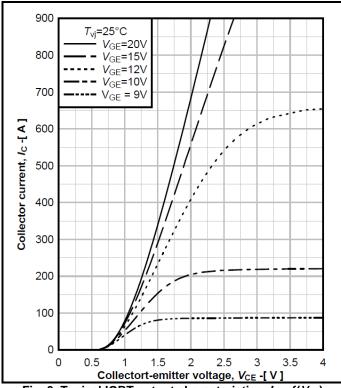


Fig. 3 Typical IGBT output characteristics, $I_C = f(V_{CE})$

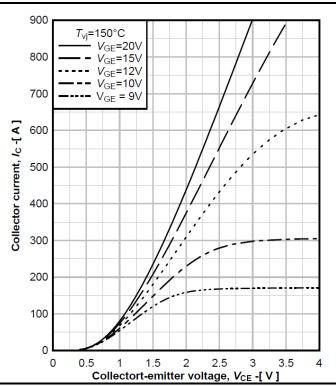


Fig. 4 Typical IGBT output characteristics, $I_C = f(V_{CE})$

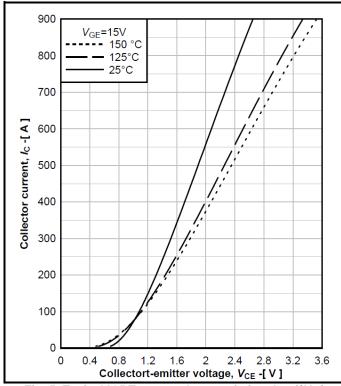


Fig. 5 Typical IGBT output characteristics, $I_C = f(V_{CE})$

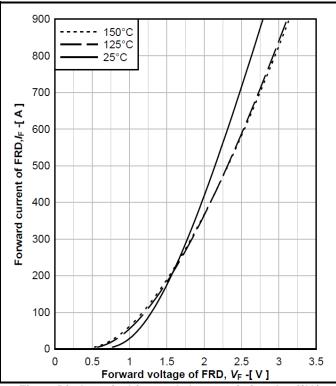
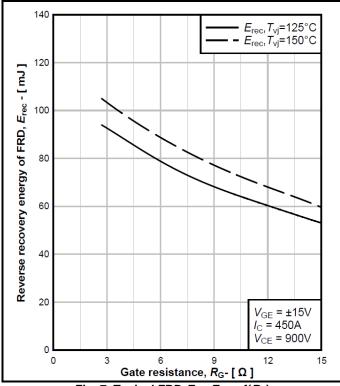


Fig. 6 Diode typical forward characteristics, $I_F = f(V_F)$







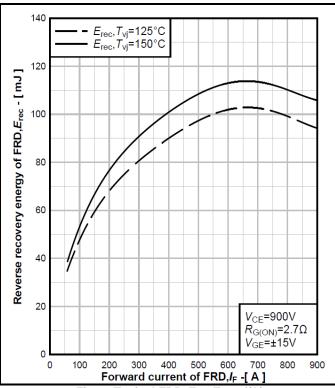


Fig. 8 Typical FRD E_{rec} , $E_{rec} = f(I_F)$

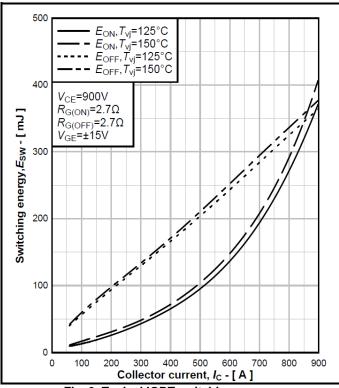


Fig. 9 Typical IGBT switching energy, $E_{ON} = f(I_C), E_{OFF} = f(I_C)$

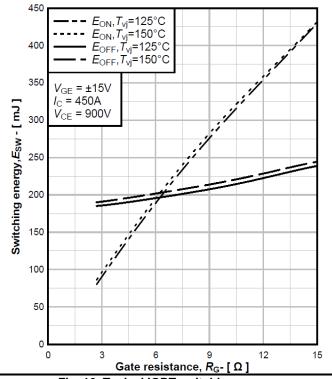


Fig. 10 Typical IGBT switching energy $E_{ON} = f(R_G)$, $E_{OFF} = fR_G$)



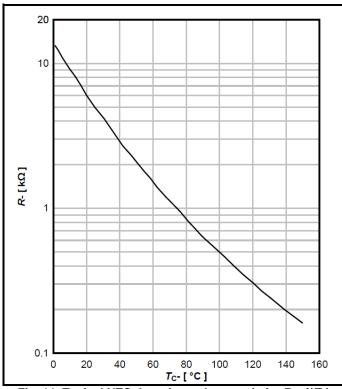


Fig. 11 Typical NTC thermistor characteristic, $R = f(T_C)$

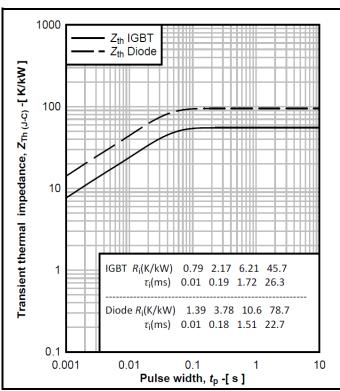


Fig. 13 Transient thermal impedance, $Z_{th}(J-C) = f(t)$

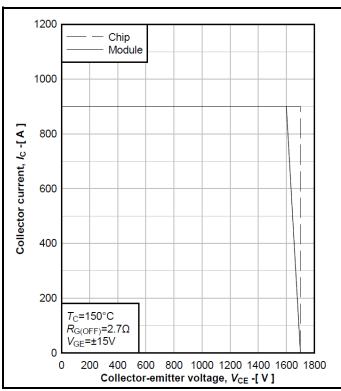


Fig. 12 Reverse bias safe operating area of IGBT, $I_C = f(V_{CE})$



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

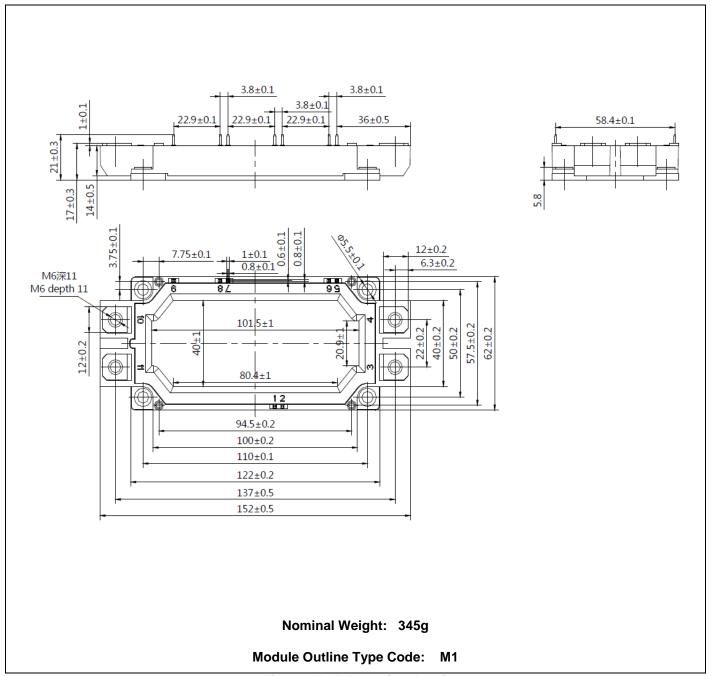


Fig. 14 Module outline drawing



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HEADQUARTERS OPERATIONS

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, Linited Kingdom

United Kingdom

Fax: +44(0)1522 500550 Tel: +44(0)1522 500500 Web: http://www.dynexsemi.com

CUSTOMER SERVICE

DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln, Lincolnshire, LN6 3LF, United Kingdom

Fax: +44(0)1522 500020

Tel: +44(0)1522 502753 / 502901 Email: <u>Power_solutions@dynexsemi.com</u>

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