

# TG600HF12M1-S3A00

# Half Bridge IGBT Module

DS6250-1 August 2018 (LN36188)

## **FEATURES**

- Cu Base with Enhanced Al<sub>2</sub>O<sub>3</sub> Substrates
- 10µs Short Circuit Withstand

#### **APPLICATIONS**

- Motor Drives
- · Power Charging Equipment
- Solar Power
- High Reliability Inverters

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The TG600HF12M1-S3A00 is a half bridge 1200V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

# **ORDERING INFORMATION**

Order As:

## TG600HF12M1-S3A00

Note: When ordering, please use the complete part number

#### **KEY PARAMETERS**

V <sub>CES</sub>		1200V
V <sub>CE(sat)</sub>	* (typ)	1.70V
l <sub>c</sub> ` ´	(max)	600A
I <sub>C(PK)</sub>	(max)	1200A

<sup>\*</sup> Measured at the auxiliary terminals

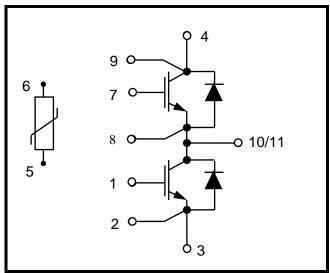


Fig. 1 Circuit configuration



Fig. 2 Package



# **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V, T <sub>C</sub> = 25°C	1200	V
$V_{GES}$	Gate-emitter voltage	T <sub>C</sub> = 25°C	±20	V
I <sub>C</sub>	Continuous collector current	T <sub>C</sub> = 88°C	600	Α
I <sub>C(PK)</sub>	Peak collector current	t <sub>P</sub> = 1ms, TC = 133°C	1200	Α
P <sub>max</sub>	Max. transistor power dissipation	T <sub>C</sub> = 25°C, T <sub>vj</sub> = 175°C	3.0	kW
l <sup>2</sup> t	Diode I <sup>2</sup> t value	$V_R = 0$ , $t_p = 10$ ms, $T_{vj} = 150$ °C	28.8	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V

#### THERMAL AND MECHANICAL RATINGS

CTI (Comparative Tracking Index):

Internal insulation material:  $Al_2O_3$ Baseplate material: Cu Creepage distance – Terminal to heatsink: 14.5mm Creepage distance – Terminal to terminal: 13.0mm Clearance - Terminal to heatsink: 12.5mm Clearance - Terminal to terminal: 10mm

**Parameter** 

Thermal resistance - IGRT °C/k\//

**Test Conditions** 

>200

K <sub>th(j-c)</sub>	Thermal resistance – IGBT	Continuous dissipation -	•	-	49	-C/KVV
R <sub>th(j-c)</sub>	Thermal resistance – diode	junction to case	-	-	77	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (IGBT)	Mounting torque 5Nm	-	-	34	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (Diode)	(with mounting grease 1W/m °C)	1	-	40	°C/kW
_	lunation to managetura	IGBT	-40	-	150	°C
Tj	Junction temperature	Diode	-40	-	150	°C
$T_{stg}$	Storage temperature range	-	-40	-	150	°C
	Covernatorena	Mounting – M5	3	-	6	Nm
	Screw torque	Electrical connections – M6	3	-	6	Nm

Symbol

Min

Тур.

**Units** 

Max



# **ELECTRICAL CHARACTERISTICS**

 $T_{case}$  = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min Typ		Max	Units
	Collector cut-off current	$V_{GE} = 0V$ , $V_{CE} = V_{CES}$			1	mA
I <sub>CES</sub>		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{C} = 125^{\circ}C$			10	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{C} = 150^{\circ}C$			20	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			0.5	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 15$ mA, $V_{GE} = V_{CE}$	5.0	6.0	7.0	V
		$V_{GE} = 15V, I_{C} = 600A$		1.7		V
$V_{CE(sat)}$	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 600A, T <sub>j</sub> = 125°C		2.0		V
	Vollago	$V_{GE} = 15V, I_C = 600A, T_j = 150$ °C		2.1		V
I <sub>F</sub>	Diode forward current	DC		600		Α
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms		1200		Α
		I <sub>F</sub> = 600A		1.9		V
$V_{F}$	Diode forward voltage	I <sub>F</sub> = 600A, T <sub>j</sub> = 125°C		2.1		V
		I <sub>F</sub> = 600A, T <sub>j</sub> = 150°C		2.1		V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 100kHz		57		nF
Qg	Gate charge	±15V		6.1		μC
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		1.8		nF
L <sub>M</sub>	Module inductance			22		nΗ
R <sub>INT</sub>	Internal transistor resistance			1		mΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	$T_{j} = 150^{\circ}\text{C}, V_{CC} = 800\text{V}$ $t_{p} \le 10\mu\text{s}, V_{GE} \le 15\text{V}$ $V_{CE \text{ (max)}} = V_{CES} - L^{*} x \text{ dI/dt}$ IEC 60747-9		2800		А

# Note:

L is the circuit inductance +  $L_{\text{M}}$ 

# **NTC-Thermistor Data**

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
R <sub>25</sub>	Rated resistance	$T_{\rm C} = 25^{\circ}{\rm C}$		5		kΩ
Δ <i>R</i> /R	Deviation of R100	$T_{\rm C} = 100^{\circ}{\rm C},  {\rm R}_{100} = 493\Omega$	-5		5	%
P <sub>25</sub>	Power dissipation	T <sub>C</sub> = 25°C			20	m/W
B <sub>25/50</sub>		$R_2 = R_{25} exp [B_{25/50}(1/T2 - 1/(298.15K))]$		3375		K
B <sub>25/80</sub>	B-value	$R_2 = R_{25} exp [B_{25/80}(1/T2 - 1/(298.15K))]$		3411		K
B <sub>25/100</sub>		$R_2 = R_{25} exp [B_{25/100}(1/T2 - 1/(298.15K))]$		3433		K



# **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time		<i>dv/dt</i> = 3500V/μs		710		ns
t <sub>f</sub>	Fall time	$\begin{array}{c} I_{C} = 600A \\ V_{CE} = 600V \\ V_{GE} = \pm 15V \\ R_{G(OFF)} = 0.5\Omega \\ R_{G(ON)} = 0.5\Omega \\ L_{S} \sim 40nH \end{array}$			280		ns
E <sub>OFF</sub>	Turn-off energy loss				90		mJ
t <sub>d(on)</sub>	Turn-on delay time		<i>di/dt</i> = 6000A/µs		185		ns
t <sub>r</sub>	Rise time				92		ns
E <sub>ON</sub>	Turn-on energy loss				22		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 600A			52		μC
I <sub>rr</sub>	Diode reverse recovery current	V <sub>CE</sub> = 600V		365		Α	
E <sub>rec</sub>	Diode reverse recovery energy	di/dt = 6	6000A/µs		29		mJ

# T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	1 6004	<i>dv/dt</i> = 3500V/μs		780		ns
t <sub>f</sub>	Fall time	$\begin{array}{c} I_{C} = 600A \\ V_{CE} = 600V \\ V_{GE} = \pm 15V \\ R_{G(OFF)} = 0.5\Omega \\ R_{G(ON)} = 0.5\Omega \\ L_{S} \sim 40 nH \end{array}$			400		ns
E <sub>OFF</sub>	Turn-off energy loss				102		mJ
t <sub>d(on)</sub>	Turn-on delay time		di/dt = 6000A/µs		180		ns
t <sub>r</sub>	Rise time				98		ns
E <sub>ON</sub>	Turn-on energy loss				32		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 600A			78		μC
I <sub>rr</sub>	Diode reverse recovery current	$V_{CE} = 600V$		420		Α	
E <sub>rec</sub>	Diode reverse recovery energy	<i>di/dt</i> = 6	6000A/µs		43		mJ

# T<sub>case</sub> = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time				800		ns
t <sub>f</sub>	Fall time	$\begin{array}{c} I_{C} = 600 A \\ V_{CE} = 600 V \\ V_{GE} = \pm 15 V \\ R_{G(OFF)} = 0.5 \Omega \\ R_{G(ON)} = 0.5 \Omega \\ L_{S} \sim 40 nH \end{array}$	<i>dv/dt</i> = 3500V/μs		430		ns
E <sub>OFF</sub>	Turn-off energy loss				105		mJ
t <sub>d(on)</sub>	Turn-on delay time		di/dt = 6000A/µs		180		ns
t <sub>r</sub>	Rise time				100		ns
E <sub>ON</sub>	Turn-on energy loss				34		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 600A			100		μC
I <sub>rr</sub>	Diode reverse recovery current	$V_{CE} = 600V$		450		Α	
E <sub>rec</sub>	Diode reverse recovery energy	di/dt = 6	6000A/µs		55		mJ



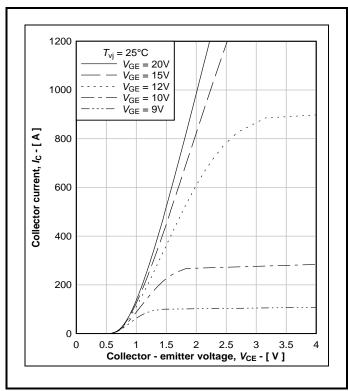


Fig. 3 Typical IGBT output characteristics,  $I_C = f(V_{CE})$ 

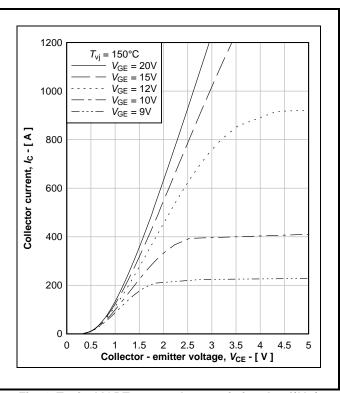


Fig. 4 Typical IGBT output characteristics,  $I_C = f(V_{CE})$ 

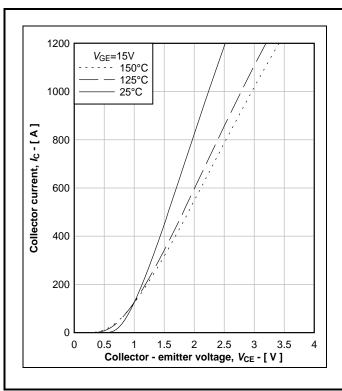


Fig. 5 Typical IGBT output characteristics,  $I_C = f(V_{CE})$ 

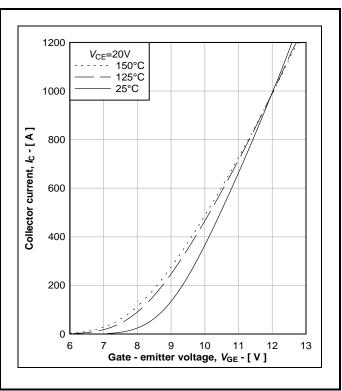


Fig. 6 Typical IGBT transfer characteristics,  $I_C = f(V_{GE})$ 



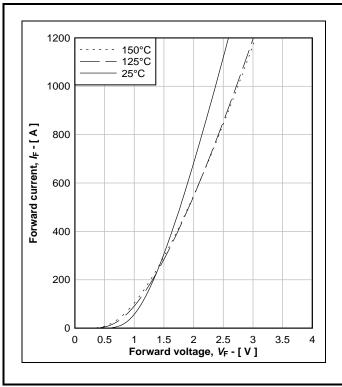


Fig. 7 Diode typical forward characteristics,  $I_F = f(V_F)$ 

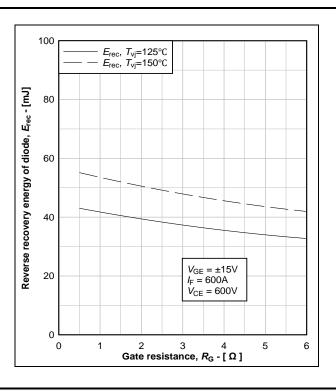


Fig. 8 Typical FRD  $E_{rec}$ ,  $E_{rec} = f(R_G)$ 

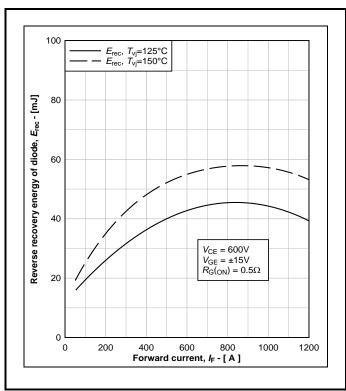


Fig. 9 Typical FRD  $E_{rec}$ ,  $E_{rec} = f(I_F)$ 

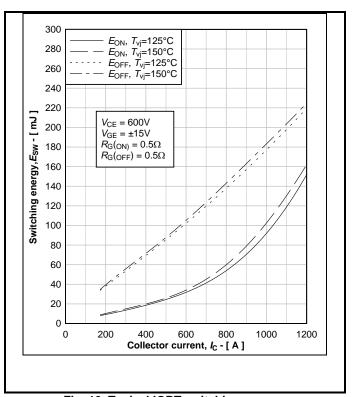


Fig. 10 Typical IGBT switching energy,  $E_{ON} = f(I_C), E_{OFF} = f(I_C)$ 



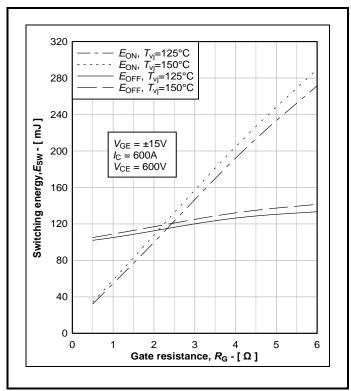


Fig. 11 Typical IGBT switching energy  $E_{ON} = f(R_G)$ ,  $E_{OFF} = fR_G$ )

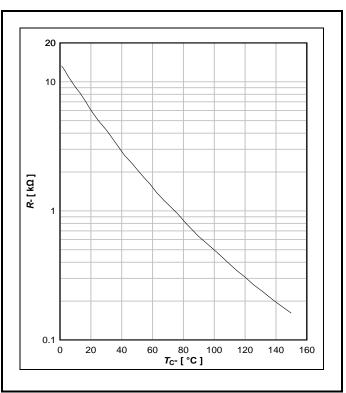


Fig. 12 Typical NTC thermistor characteristic,  $R = f(T_C)$ 

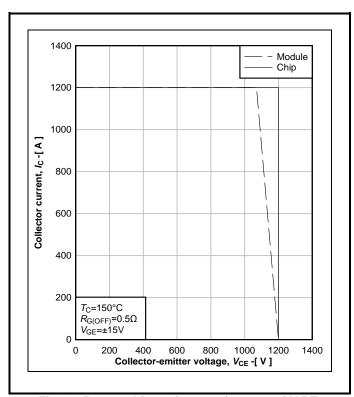


Fig. 13 Reverse bias safe operating area of IGBT,  $I_C = f(V_{CE})$ 

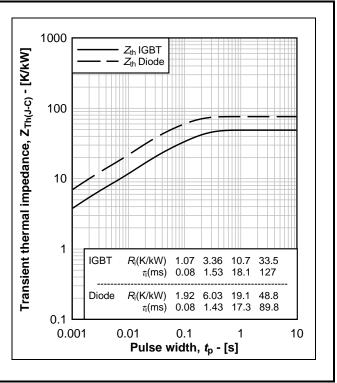


Fig. 14 Transient thermal impedance,  $Z_{th}(J-C) = f(t)$ 



## **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

# DO NOT SCALE.

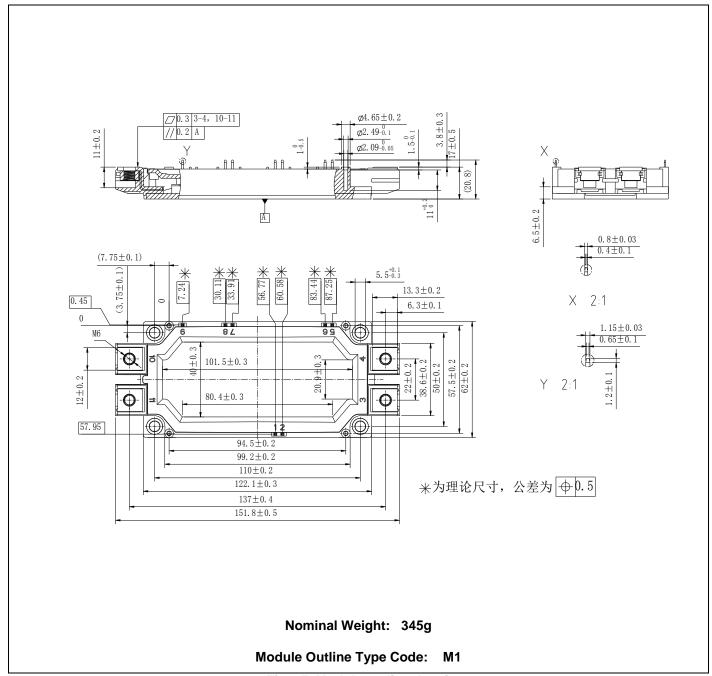


Fig. 15 Module outline drawing



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